

DPS2014 TIMETABLE(tentative)

November 27

8:00	Registration	
8:10		
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9:00	Opening Remark (Hayashi) Young Award, Nishizawa Award (Nakano)	
9:10	A-1 <Invited>	Session A Plasma Processes for New Applications
9:20	Dr. Itaru Yanagi (Hitachi Ltd.)	
9:30	"A novel method for fabricating nanopores with diameters of sub-1 nm to 3 nm"	
9:40	A-2 <Invited>	
9:50	Dr. Meng-Jiy Wang (National Taiwan University of Science and Technology)	
10:00	"Plasma surface modifications for modulating drug release and cell proliferation"	
10:10	A-3 <Invited>	
10:20	Dr. Rui Zhang (Univ. of Tokyo)	
10:30	"MOS interface passivation of Ge MOSFETs by plasma post oxidation"	
10:40	Break	
10:50	B-1 M. Matsui,	Session B Etching and Surface Reaction
11:00	"Analysis of surface reaction layers formed by Si ₃ N ₄ etching by pulsed microwave plasma"	
11:10	B-2 T. Sasaki,	
11:20	"Highly selective etching of LaAlSiO _x to Si using C ₄ F ₈ /Ar/H ₂ plasma"	
11:30	B-3 Hu Li,	
11:40	"Etching characteristics of transparent conducting oxides by methane-based plasmas"	
11:50	B-4 K. Karahashi,	
12:00	"Etching reactions of magnetic materials by CO cluster beams"	
12:10	B-5 K. Eriguchi,	
12:20	"A model for plasma-induced latent defects in three-dimensional structures and its application to parameter variation analysis of FinFETs"	
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13:00	Lunch	
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13:40	C-1 <Invited>	Session C Plasma Monitoring and Simulation
13:50	Dr. Keiji Nakayama (Institute of Mesotechnology)	
14:00	"Atmospheric pressure plasma generated in a gap of dry and oil lubricated sliding"	
14:10	C-2 <Invited>	
14:20	Dr. Laxminarayan Raja (Univ. of Texas at Austin)	
14:30	"Computational simulations of streamer discharges for reactive plasma applications at atmospheric pressures"	
14:40	C-3 A. Pandey,	
14:50	"Time-resolved curling-probe measurement of electron density in pulsed discharge"	
15:00	C-4 N. Kuboi,	
15:10	"Prediction of Plasma-Induced Damage Distribution during Silicon Nitride Etching Using Advanced 3D Voxel Model"	
15:20	C-5 T. Kawanabe,	
15:30	"Controlling ashing rate distribution by introducing additional gas flow in a 450-mm-wafer ashing reactor"	
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15:50	Break	
16:00	D-1 <Invited>	Session D Dry Etching Technology for Advanced Patterning
16:10	Dr. Hironobu Sato (EIDEC)	
16:20	"Sub-10nm directed self-assembly lithography using high chi block copolymers"	
16:30	D-2 L. Azarnouche,	
16:40	"Interactions between plasma and organosilicon polymers for directed self-assembly patterning applications"	
16:50	D-3 Yan Zhang,	
17:00	"Surface Modifications of Photoresist Polymers under Photon Irradiations Emitted from HBr Plasma"	
17:10	D-4 J.H. Liao,	
17:20	"Etch Challenges for Metal Hard Mask Open for 14 nm Node"	
17:30	D-5 O. Ros,	
17:40	"Cure free plasma processes for improved gate patterning and LWR for 14nm FDSOI technologies"	
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18:00	Banquet	
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November 28

8:00	Registration	
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9:00	E-1 <Invited>	Session E CVD/PVD/ ALD
9:10	Dr. Yasuaki Ishikawa (NAIST)	
9:20	"ALD's oxidant effect on characteristics as an oxide dielectric"	
9:30	E-2 <Invited>	
9:40	Dr. Paul Poodt (TNO/Holst Centre)	
9:50	"Spatial Atomic Layer Deposition for large-area and flexible electronics"	
10:00	E-3 M. Noma,	
10:10	"Effects of ion energy on surface and mechanical properties of BN films formed by a reactive plasma-assisted coating method"	
10:20	E-4 S. Nunomura,	
10:30	"In-situ monitoring of carrier transport in semiconductor active layer under plasma processing"	
10:40	E-5 Y. Setsuhara,	
10:50	"Process Controllability of ICP-Enhanced Reactive Sputter Deposition for Low-Temperature Formation of IGZO TFT"	
11:00	Break	
11:10	Poster	
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13:00	Lunch	
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13:50	F-1 T. Ikeda,	Session F Plasma Induced Damage
14:00	"Experimental evidence of layout-dependent low-k damage during plasma processing -Role of"	
14:10	F-2 M. Fukasawa,	
14:20	"Systematic comparison of various analytical techniques for evaluating plasma-induced damage"	
14:30	F-3 T. Iwai,	
14:40	"Low-temperature microwave repairing for plasma-induced local defect structures near Si substrate surface"	
14:50	F-4 Z. Liu,	
15:00	"Damage-formations in GaN processed at high temperatures"	
15:10	F-5 T. Shigetoshi,	
15:20	"Control of Si-SiO ₂ Interface Defects Generation during Thin Dielectric Film Etching Using CH ₃ F/Ar/O ₂ Plasma"	
15:30		
15:40	Break	
15:50	G-1 <Invited>	Session G Atomic Layer Etching
16:00	Dr. Shahid Rauf (Applied Materials Inc.)	
16:10	"Atomic precision etch for semiconductor manufacturing"	
16:20	G-2 <Invited>	
16:30	Dr. Geun Young Yeom (Sungkyunkwan University)	
16:40	"Application of atomic layer etching for Si-based devices"	
16:50	G-3 A. Agarwal,	
17:00	"Modeling of Low Electron Temperature Plasmas to Enable Atomic Layer Etching"	
17:10	G-4 T. Lill,	
17:20	"Plasma-Enhanced Adsorption for Atomic Layer Etch of Silicon"	
17:30	Closing Remark (Okumura)	
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